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DRLWA
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No. 230074-0223

In re patent application of

HUYNH, ET AL.

Group Art Unit: 2131

Serial No. 09/503,282

Examiner: H. Song

Filed: February 14, 2000

For: PACKET PROCESSOR

AMENDMENT AND REMARKS UNDER 37 C.F.R. § 1.111

Commissioner for Patents
Washington, D.C. 20231

Commissioner:

In reply to the Office Action mailed June 6, 2001, please amend the above-identified application as follows:

IN THE CLAIMS

Claims 1-7, 9, 15, 16, 22, 23, and 29-31 are amended as follows:

1. (Amended) A packet processor comprising:
- a control unit having a data input bus;
 - at least one encryption processing unit;
 - a first authentication processing unit;
 - a second authentication processing unit;
 - a local data bus, independent of the data input bus to the control unit, coupling the control unit to each of the encryption and authentication processing units; and
 - a second data bus from the encryption processing unit to each authentication processing unit, including a data bus from the first authentication processing unit to the second authentication processing unit.